

10/801, 993.

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August 23, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: Title: SOI CMOS DEVICE WITH REDUCED DIBL
Letters Patent No. 6,872,640
Issued: March 29, 2005
Our Reference: MICRON.261A

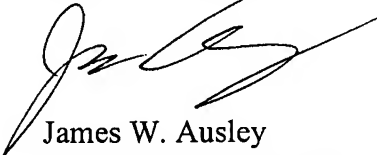
Dear Sir:

Enclosed for filing is a Certificate of Correction in connection with the above-identified patent.

As the errors cited in the Certificate of Correction were incurred through the fault of the Patent Office, no fee is believed to be required. However, please charge our Deposit Account No. 11-1410 for any fees that may be incurred with this request.

Respectfully submitted,

Knobbe, Martens, Olson & Bear, LLP


James W. Ausley
Registration No. 49,076
Customer No. 20,995

Enclosures

PA-014

1881939
082205

Certificate
AUG 31 2005
of Correction

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Orange County
949-760-0404

San Diego
619-235-8550

San Francisco
415-954-4114

Los Angeles
310-551-3450

San Luis Obispo
805-547-5580

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,872,640

DATED : March 29, 2005

INVENTOR(S): Mouli

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1, line 7 delete "(SI)" and insert -- SOI --

Column 2, line 47 delete "farther" and insert -- further --

Column 3, line 45 delete "(SOD)" and insert -- SOI --

Column 14, claim 27, line 58 delete "swells" and insert -- p-wells --

Column 14, claim 29, line 64 delete "arc" and insert -- are --

Column 16, claim 37, line 21 delete "dopant" and insert -- dopants --

Column 16, claim 37, line 23 delete "device" and insert -- devices --

MAILING ADDRESS OF SENDER:

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PATENT NO. 6,872,640

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August 23, 2005

MICRON.261A
FORM PTO 1050

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